History of Project $/vmod

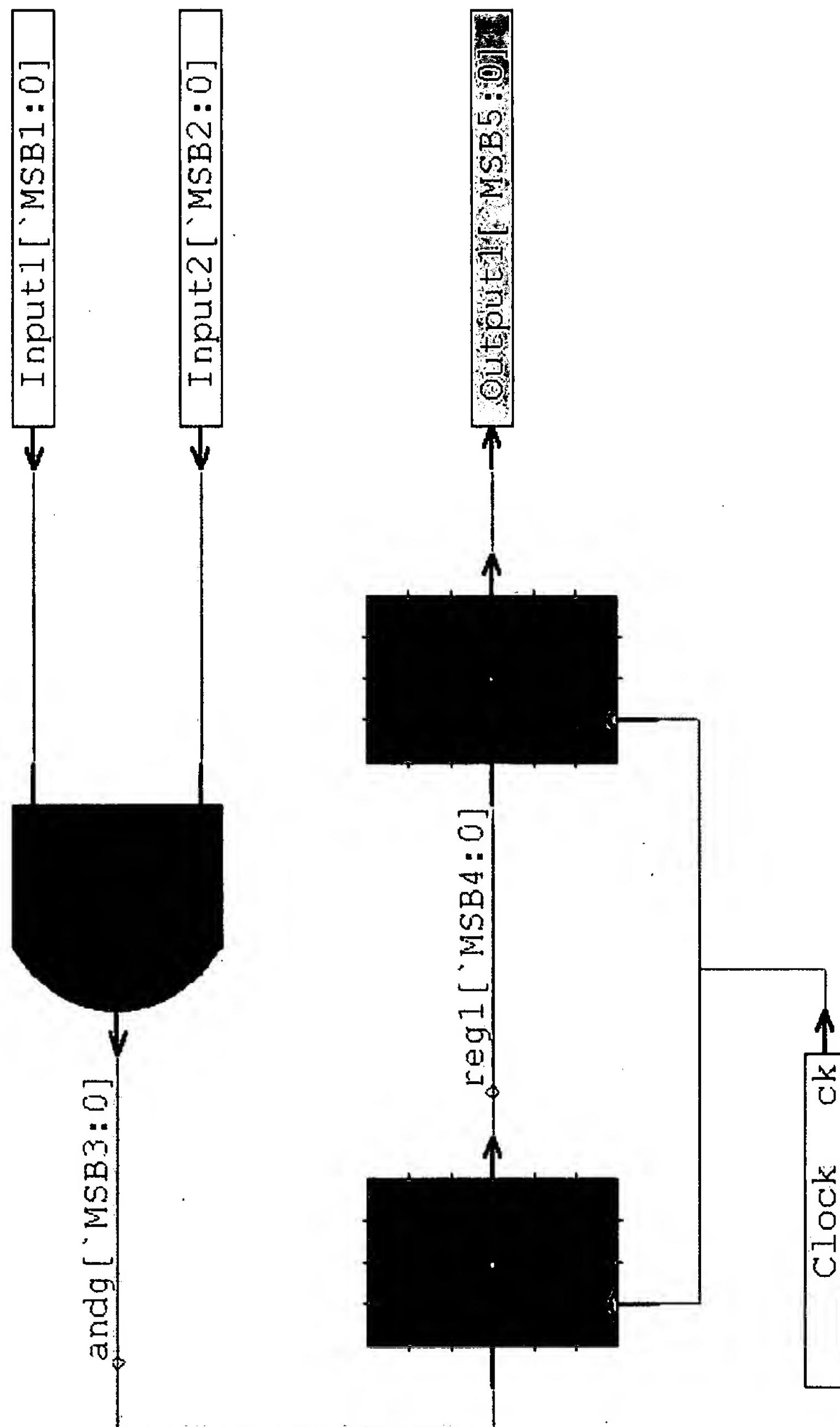|		
$/	Bwheeler	Labeled Version 0.141
version.h	Bwheeler	Checked in $/vmod
M_sim_event_high_trig.cpp	Bwheeler	Checked in $/vmod
M_sim_event_inst.cpp	Bwheeler	Checked in $/vmod
M_sim_event_low.cpp	Bwheeler	Checked in $/vmod
M_sim_event_low_trig.cpp	Bwheeler	Checked in $/vmod
M_sim_event_rise.cpp	Bwheeler	Checked in $/vmod
M_sim_event_rise_slave.cpp	Bwheeler	Checked in $/vmod
vmod.opt	Bwheeler	Checked in $/vmod
M_sim_event_async_trig.cpp	Bwheeler	Checked in $/vmod
M_sim_event_async_trig.h	Bwheeler	Checked in $/vmod
M_sim_event_clk_edge.cpp	Bwheeler	Checked in $/vmod
M_sim_event_clk_level.cpp	Bwheeler	Checked in $/vmod
M_sim_event_clk_level.h	Bwheeler	Checked in $/vmod
M_sim_event_fall.cpp	Bwheeler	Checked in $/vmod
M_sim_event_fall_slave.cpp	Bwheeler	Checked in $/vmod
M_sim_event_high.cpp	Bwheeler	Checked in $/vmod
M_node.h	Bwheeler	Checked in $/vmod
m_node_etc.h	Bwheeler	Checked in $/vmod
M_sim_event.cpp	Bwheeler	Checked in $/vmod
M_sim_event.h	Bwheeler	Checked in $/vmod
M_sim_event_async.cpp	Bwheeler	Checked in $/vmod
M_sim_event_async.h	Bwheeler	Checked in $/vmod
M_net_timing.cpp	Bwheeler	Checked in $/vmod
M_net_timing.h	Bwheeler	Checked in $/vmod
M_node.cpp	Bwheeler	Checked in $/vmod
M_net.cpp	Bwheeler	Checked in $/vmod
M_module.h	Bwheeler	Checked in $/vmod
M_module.cpp	Bwheeler	Checked in $/vmod
M_gate_OR.cpp	Bwheeler	Checked in $/vmod
M_gate_port.cpp	Bwheeler	Checked in $/vmod
M_gate_port.h	Bwheeler	Checked in $/vmod
M_gate_reg.cpp	Bwheeler	Checked in $/vmod
M_gate_src_sink.cpp	Bwheeler	Checked in $/vmod
M_gate_XOR.cpp	Bwheeler	Checked in $/vmod
M_gate_buf.cpp	Bwheeler	Checked in $/vmod
M_gate_buf.h	Bwheeler	Checked in $/vmod
M_gate_inst.cpp	Bwheeler	Checked in $/vmod
M_gate_mux.cpp	Bwheeler	Checked in $/vmod
M_gate.h	Bwheeler	Checked in $/vmod
M_gate_AND.cpp	Bwheeler	Checked in $/vmod
M_gate_array.cpp	Bwheeler	Checked in $/vmod
M_gate_block_sensitivity.cpp	Bwheeler	Checked in $/vmod
M_gate_block_neg_edge_clocked.cpp	Bwheeler	Checked in $/vmod

History of $/vmod version.h

		User		Action
66		Bwheeler		Checked in $/vmod
		Tfennel		Labeled 'Tim060401'
		Tfennel		Labeled 'Tim060101'
		Tfennel		Labeled 'Tim053001'
		Bwheeler		Labeled 'version 0.140'
65		Bwheeler		Checked in $/vmod
		Tfennel		Labeled 'Tim0522010'
		Tfennel		Labeled 'Tim0522018'
		Tfennel		Labeled 'Tim052201'
		Tfennel		Labeled 'Tim0521'
		Tfennel		Labeled 'Tim051801'
		Bwheeler		Labeled 'version 0.139'
64		Bwheeler		Checked in $/vmod
		Tfennel		Labeled 'Tim051701'
		Tfennel		Labeled 'Tim051601'
		Bwheeler		Labeled 'Wrk before uptdate 5/11'
		Bwheeler		Labeled 'version 0.138'
63		Bwheeler		Checked in $/vmod
		Tfennel		Labeled 'Tim050401'
		Bwheeler		Labeled 'version 0.137'
62		Bwheeler		Checked in $/vmod
		Tfennel		Labeled 'Tim050101'

Exhibit C

Input1[`MSB1:0]

Input2[`MSB2:0]

Output1[`MSB5:0]

andg[`MSB3:0]

reg1[`MSB4:0]

Clock ck

	Translated String
Global 'define Definitions	
DEF LMSB1 -> "8"	8
DEF LMSB2 -> "24"	24
DEF MSB1 -> "30"	30
DEF MSB2 -> "31"	31
DEF MSB3 -> "31"	31
DEF MSB4 -> "31"	31
DEF MSB5 -> "`LMSB1 + `LMSB2"	32

Exhibit D

Input1[`MSB1:0]

Input2[`MSB2:0]

Output1[`MSB3:0]

andg[`MSB3:0]

`define Definition   [x]

`define name: MSB1

`define definition: 30

OK   Cancel

Clock ck

	Translated String
☐ Global `define Definitions	
DEF LMSB1 -> "8"	8
DEF LMSB2 -> "24"	24
DEF MSB1 -> "30"	30
DEF MSB2 -> "31"	31
DEF MSB3 -> "31"	31
DEF MSB4 -> "31"	31
DEF MSB5 -> "`LMSB1 + `LMSB2"	32

Exhibit E

Input1[`MSB1:0]

Input2[`MSB2:0]

Output1[`MSB5:0]

andg[`MSB3:0]

reg1[`MSB4:0]

Clock ck

32
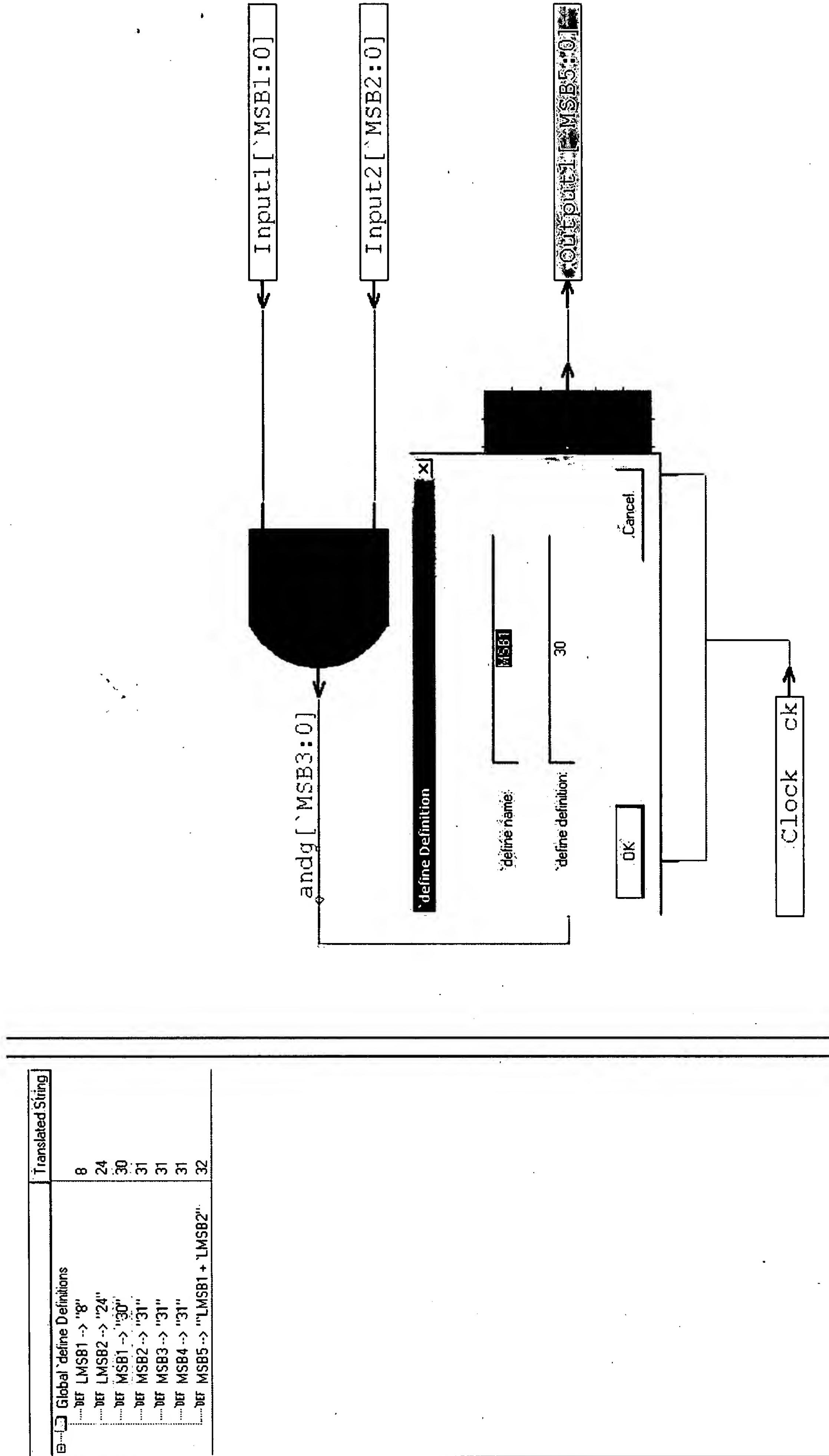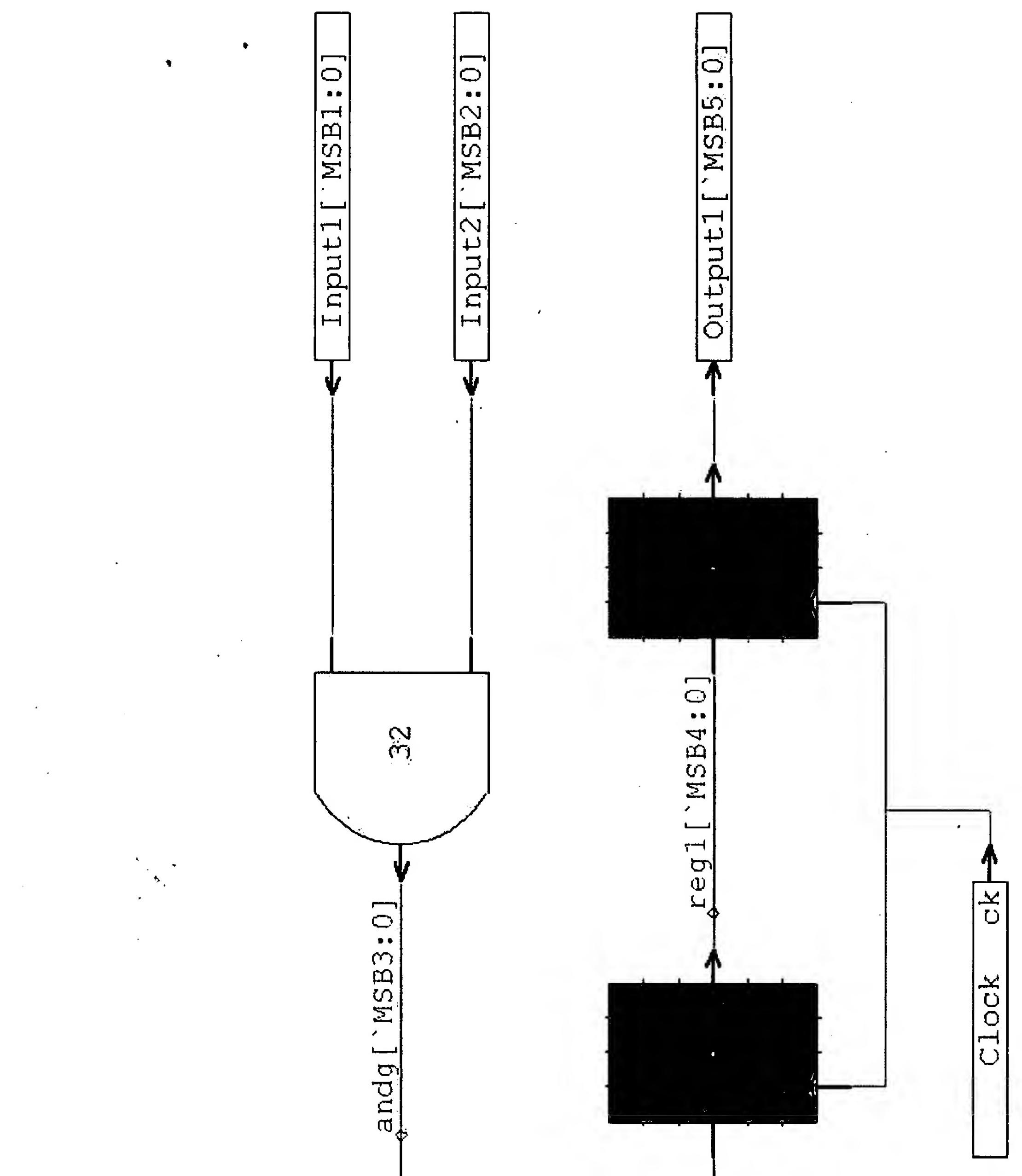
	Translated String
Global `define Definitions	
DEF LMSB1 --> "8"	8
DEF LMSB2 --> "24"	24
DEF MSB1 --> "31"	31
DEF MSB2 --> "31"	31
DEF MSB3 --> "31"	31
DEF MSB4 --> "31"	31
DEF MSB5 --> "LMSB1 + LMSB2"	32